

L Number	Hits	Search Text	DB	Time stamp
-	19	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:20
-	33	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:20
-	16	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command) and pci	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:21
-	13	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command) and pci and bus and power and input	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:24
-	3	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech) adj1 command) and pci and bus and power and input	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:24
-	38	(motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:56
-	11	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:00
-	10	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls. and bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:54
-	13	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:52
-	47	(motherboard PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:58
-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:02
-	1	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls. and pci	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:02
-	24	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:10

-	20	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:54
-	32	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and (memory same dsp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:11
-	23	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and bus and (memory same dsp) and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:19
-	1	"05844824"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:19
-	24	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and bus and (memory same dsp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:21
-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 10:41
-	34	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and speech	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 10:42
-	29	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:06
-	20	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 15:57
-	19	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power) and dynamic\$5 and command	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:05
-	1	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power) and dynamic\$5 and command and phoneme	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:00
-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:15

-	42	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:17
-	29	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recogni\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:15
-	32	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:18
-	11	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme and (continuous adj1 speech) and (voice adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 18:19
-	20	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:50
-	6	"06154830" "06085314"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 18:24
-	2	"06170049"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 18:24
-	20	((motherboard mother-board (mother board) PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:53
-	10	((motherboard mother-board) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:51
-	20	((motherboard mother-board (mother board) PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:53
-	11	((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:55
-	87	PCI and PDA and dsp and cpu	USPAT; US-PGPUB	2003/04/18 21:28
-	36	PCI and PDA and dsp and cpu and ((speech voice) adj1 recognition)	USPAT; US-PGPUB	2003/04/18 22:42
-	9	PCI and ((cellular mobile) adj1 phone) and (speech adj1 recognition) and dsp	USPAT; US-PGPUB	2003/04/18 22:55

-	3	PCI and ((cellular mobile) adj1 phone) and vocabulary	USPAT; US-PGPUB	2003/04/18 22:56
-	46	704/\$.ccls. and ((mobile (phone telephone)) or (cellular phone)) and DSP and (noise adj1 cancel\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 20:11
-	4	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing)	USPAT; US-PGPUB	2003/04/19 21:03
-	7	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 21:05
-	0	(menu near1 select\$) and (voice adj1 dialing) and (noise adj1 cancellation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 21:29
-	5	(voice adj1 dialing) and (dsp (dsp adj1 chip)) and (menu adj1 select\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 22:54
-	5	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 23:05
-	13	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 23:10
-	20	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 23:14
-	5	((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech adj1 recognition)) and (dsp same (speech adj1 recognition))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 23:13
-	1	(noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/19 23:14